



In re Application of:

Gurtej S. Sandhu

Serial No.: 10/649,058

Filed: August 27, 2003

For: VERTICAL TUNNELING
TRANSISTOR

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Group Art Unit: 2814

Examiner: Pham, Long

Atty. Docket: MICS:0099/FLE
01-1327

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

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37 C.F.R. 1.8

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September 22, 2004
Date

Helen Tinsley
Helen Tinsley

Sir:

RESPONSE TO REQUIREMENT FOR RESTRICTION

In response to the Requirement for Restriction mailed August 30, 2004, please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 16-25, without prejudice.

1. (Original) A vertical tunneling transistor, comprising:

a channel disposed on a substrate;

a quantum dot disposed so that the channel is between the quantum dot and the substrate;

a gate disposed so that the quantum dot is between the gate and the channel; and